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R-360-2017

FACULTY OF SCIENCE

B.Sc. (Second Year) (Third Semester) EXAMINATION APRIL/MAY, 2017

COMPUTER SCIENCE

Paper VI

(Digital Electronics and 8085 Microprocessor)

(MCQ & Theory)

(Saturday	, 6-5-2	017)		11me: 2.00 p.m. to 4.00 p.m.		
Time—2 H	lours			Maximum Marks—40		
<i>N.B.</i> :— ○	(i) A	$\it ll$ questions are comp	ulsory.			
(1	ii) F	igures to the right ind	icate full ma	arks.		
	Still (A)	M	CQ			
1. Mult	iple Cl	hoice Questions :		10		
(i)	18 75 - 67	logic	gates are cal	led basic gates.		
517000 10000	(a)	NAND, NOR	(b)	AND, OR, NOT		
15 V P P P P V V	(c)	Ex-OR, Ex-NOR	(d)	All of these		
(ii)	Y =	A ⊕ B is a logic equati	on of	gate.		
	(a)	OR	(<i>b</i>)	AND		
	(c)	Ex-OR	(d)	All of these		
(iii)	Maximum possible cells in four variable K-map are					
	(a)	Two	(<i>b</i>)	Four		
12 12 12 12 13 13 13 13 13 13 13 13 13 13 13 13 13	(c)	Eight	(d)	Sixteen		

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	(iv)	$\overline{A\cdot B}$	$\overline{A \cdot B} = \dots$ in boolean Algebra.					
		(a)	$\overline{A} + \overline{B}$	(b)	$\overline{A} - \overline{B}$			
		(c)	AB	(d)	All of these			
	(v)	T-FF	T-FF is called flip-flop.					
		(a)	Temporary flip-flop	(b)	Toggle flip-flo	op		
		(c)	Time flip-flop	(d)	All of these			
	(vi)	A + 1	A + 1 = in boolean Algebra.					
		(a)	0	(b)		10 1 1 1 1 0 0 0 0 0 1 1 1 1 1 1 1 1 1		
		(c)	A	(d)	All of these	15000 25001x		
	(vii)	Word size of 8085 microprocessor is bit.						
		(a)	Eight	(b)	Sixteen			
		(c)	Four	(d)	None of thes	e		
	(viii)	LDA address instruction is byte.						
	300	(a)	One	(b)	Two			
	12 10 17 18 18 18 18 18 18 18 18 18 18 18 18 18	(c)	Three	(d)	All of these			
Š	(ix)	8085	microprocessor is	pin IC.				
\$25\A		(a)	Eight	(b)	Sixteen			
		(c)	Forty	(d)	None of thes	e		
	(x)	Program counter is a bit register.						
2 9 9 7 3 9 5 7 X		(a)	Eight	<i>(b)</i>	Sixteen			
9 9 7 TY		(c)	Twenty	(d)	None of thes	e		
NA NA	00 69 71 V	NO BY	The	eory				
2.	(a)	What detail	is logic gate ? Ex	plain AND,	OR, NOT	logic gat	es in 10	
2 B 8 V	1000	13.92.13						

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		Or A TOTAL OF THE PROPERTY OF	1 / 12 / 12 / 12 / 12 / 12 / 12 / 12 /
	(<i>b</i>)	Reduce the following equation using K-map:	5
		$Y = \overline{A}\overline{B}\overline{C}\overline{D} + \overline{A}B\overline{C}\overline{D} + AB\overline{C}\overline{D} + A\overline{B}\overline{C}\overline{D} + \overline{A}BCD + ABCD.$	
	(c)	Explain addition, subtraction, multiplication laws in Bo	olean
		Algebra.	5
3.	(a)	Explain two variable, three variable and four variable K-map	with
		example.	10
			90
	(<i>b</i>)	Explain SRFF in detail.	5
	(c)	Write a program for addition of two 8-bit numbers in	8085
		microprocessor.	5
4.	(a)	Explain pin diagram of 8085 microprocessor.	10
	(<i>b</i>)	Explain any four logical instructions of 8085 microprocessor.	5
	(c)	Explain flag registers of 8085 microprocessor.	5