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AO—355—2018

FACULTY OF SCIENCE

B.Sc. (Third Semester) EXAMINATION

MARCH/APRIL, 2018

COMPUTER SCIENCE

Paper VI

(Digital Electronics and 8085 Microprocessor)

(MCQ+Theory)

(Saturday, 28-4-2018)

Time: 2.00 p.m. to 4.00 p.m.

Maximum Marks-40

Time—2 Hours

300

(MCQ)

- 1. (i) The output of an OR gate of LOW when
 - (A) any input is LOW
- (B) any input is HIGH
- (C) all inputs are LOW
- (D) all inputs are HIGH
- (ii) The Boolean expression for a 3-input AND gate is
 - (A) X = AB

- (B) X = ABC
- $(C) \quad X = A + B + C$
- (D) X = AB + C
- (iii) How many cells are required in K-map for 3 variables?
 - (A) 1

 $(B) \quad 2$

(C) 4

- (D) 8
- (iv) The simplest equation which implements the K-map shown below is:

110	$\bar{\mathbf{c}}$	C
${f ar A}{f ar B}$	0	0
ĀВ		1
AB	1	1
$A\overline{f B}$	0	1

- $(A) \quad X = AC + B$
- (B) $X = A\overline{B}$
- (C) $AB\overline{C} + ABC + A\overline{B}C$
- (D) $AB + \overline{A}B$

P.T.O.

		`	,		
(<i>v</i>)	A J-	K flip-flop is in a 'no-cha	ange' c	ondition when	
	(A)	J = 1, K = 1	(B)	J = 1, K = 0	
	(C)	J = 0, K = 1	(D)	J=0, K=0	
(<i>vi</i>)	Popular application flip-flop are ?				
	(A)	Counters			
	(B)	Shift registers		\$ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	
	(C)	Transfer registers		999946574986699 9,9,0,6,6,6,7,4,3,8,4,6,8,2,6,8,	
	(D)	All of the above			
(vii)	Register is a:				
	(A)	Set of capacitor used to computer	o regist	ter input instructions in a digital	
	(B)	Set of paper tapes and	cards	put in a file	
	(C)	Temporary storage uni	t with	in the CPU having dedicated or	
	(D)	Part of the auxiliary r	nemory		
(viii)	A si	mple flip-flop:			
63.3	(A)	is a 2 bit memory		2000 2000	
	(B)	is a 1 bit memory			
	(C)	is a four state device	A Sign		
	(D)	has nothing to do with	memo	ory	
(ix)	The communication line between the CPU, memory and peripherals				
	is ca	lled a			
10000 00001	(A)	Bus	(B)	Line	
	(C)	Media	(D)	None of these	
(x)	The	8085 microprocessor uses	s	power supply.	
Y 500	(A)	+ 5 V	(B)	– 5 V	
3/3/17/2	C	39 V	(D)	19 V	

WT		(3)	AO—355—2018				
		(Theory)					
2.	Expl	Explain the universal properties of NAND and NOR operation with truth table					
	and	circuit diagram.	10				
		Or STATE OF THE ST					
	(a)	Explain the two variable Karnaugh map method w	ith example. 5				
	(<i>b</i>)	Explain don't care condition.	5				
3.	Drav	w the circuit diagram of the Ripple counter and explain it	s operation. Give				
	its t	iming diagram.	10				
	(a)	Explain the concept of RS flip-flop.	5				
	(<i>b</i>)	Explain the concept of 1-bit memory cell.	5				
4.	Expl	lain the architecture of 8085 microprocessor.	10				
	(a)	What is microprocessor? Explain in detail.	5				
	(<i>b</i>)	Explain microprocessor operation.	5				